# Fifth Generation Computer Systems

*T. Moto-Oka*



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**Fifth Generation Computer Systems** T. Moto-Oka
The Japan Information Processing Development Centre (JIPDEC) established a committee for Study and Research on Fifth-Generation Computers. Beginning in 1979, this Committee set out on a two-year investigation into the most desirable types of computer systems for application in the 1990`s (fifth-generation computers) and how the development projects aimed at the realization of these systems should be carried forward.

This book contains the papers presented at the International Conference on Fifth Generation Computer Systems. Included among these papers is a preliminary report on the findings of the Committee.

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